Today, they’re some of the talented professionals who put Teradyne at the top of the Very Large Scale Integration (VLSI) test market. Not too long ago, they were MIT students. And on Tuesday, February 19th, they’re “coming home” to share their experience and expertise with you. To tell you about their career choice and status. About our backplane connection systems, and to give you a demonstration of the Teradyne software that’s eating up the competition in the ATE industry. And to answer your questions about how Teradyne might help you achieve your professional goals.

From Boston
- Marc Levine ’79
- Joe Wrinn ’75

From Nashua, NH
- Terry Sutton ’83

Tuesday, February 19th
From 7–9PM
Come to Building 5, Room 134
Refreshments will be served.

In any division, it’s people like these who have put Teradyne at the top of telephone system testing, backplane connection systems and state-of-the-art circuit board technology for VLSI packaging requirements, and more. It’s people like you we’re counting on to keep us there.

So be there, February 19th. And keep your calendar clear Monday and Wednesday, February 25th and 27th too. That’s when Teradyne will be holding on-campus interviews. Details to follow.

We are an equal opportunity employer m/f.